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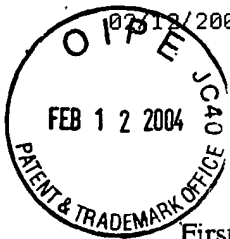
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: RUTTEN, Ivo

Application No.: 10/005,974 Conf.: 2642

Date Filed: 11/08/2001

Notice of Allowance Date:

Docket No.: US01 8179

Art Unit: 2829

Examiner: Pert, Evan T.

Title: Preconditioning Integrated Circuit for Integrated Circuit Testing

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

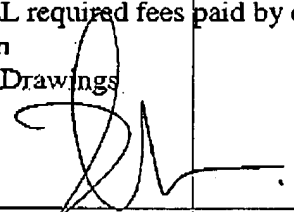
Alexandria, VA 22313-1450

Facsimile: (703) 746-4000

**CERTIFICATE OF TRANSMISSION**  
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I hereby certify that this correspondence is being facsimile transmitted to the United States and Trademark Office at facsimile number (703) 746-4000, on February 12, 2004. The documents listed below are included in this transmission:

- ✓ Issue Fee Transmittal - ALL required fees paid by deposit account
- ✓ Letter to Official Draftsman
- ✓ Three (3) sheets of Formal Drawings

  
(Signature)

Daniel L. Michalek

(Typed or printed name of person signing Certificate)

**PHILIPS ELECTRONICS NORTH AMERICA CORPORATION**

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02/12/2004 15:50

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PAGE 03/06

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: RUTTEN, Ivo  
Application No.: 10/005,974 Conf.: 2642  
Date Filed: 11/08/2001  
Notice of Allowance Date: 11/12/2003

Docket No.: US01 8179  
Art Unit: 2829  
Examiner: Pert, Evan T.  
Customer No.: 24738

Title: PRECONDITIONING INTEGRATED CIRCUIT FOR INTEGRATED CIRCUIT  
TESTING

Box ISSUE FEE  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## LETTER TO OFFICIAL DRAFTSMAN

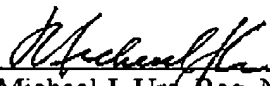
Sir:

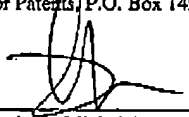
Enclosed please find three (3) sheets of corrected/substitute drawings for entry in the  
above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any  
overpayment to Deposit Account No. 14-1270.

Date: 2/12/04

Respectfully submitted,

By   
Michael J. Ure, Reg. No. 33,089  
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|   | (Name) Daniel L. Michalek   |